

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re divisional patent application of 09/387,268 filed on August 31, 1999

Yamauchi, K.

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: SEMICONDUCTOR LASER DIODE CHIP AND ITS POSITIONING AND  
MOUNTING METHOD

Assistant Commissioner of Patents  
Washington, D.C. 20231

**PRELIMINARY AMENDMENT**

Sir:

Prior to examination on the merits and calculation of the filing fee, please amend the  
above-identified application as follows:

**IN THE SPECIFICATION:**

Page 1, before line 3, insert --The present Application is a Divisional Application of  
U.S. Patent Application No. 09/387,268, filed on August 31, 1999.--.

**IN THE CLAIMS:**

**Please cancel claims 10-20 without prejudice or disclaimer.**

**REMARKS**

Claims 10-20 have been canceled to allow prosecution of claims 1-9. No new matter  
has been added.

With respect to the prior co-pending U.S. application Serial No. 09/387,268,  
Applicant hereby incorporates the claim of priority from Japanese Application 249331/1998

249331/98

filed on September 3, 1998, and claimed under 35 U.S.C. §119.

The prior application is assigned of record to NEC Corporation on August 31, 1999 at Reel 010223 and Frame 0277.

Early and favorable prosecution on the merits is respectfully requested.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,



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